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SEP 02 2004
U S P T O
P O C K E T N O . : 50099-238

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Shigenobu MAEDA, et al. : Confirmation Number: 6603
Serial No.: 10/748,273 : Group Art Unit: 2811
Filed: September 2, 2004 : Examiner:
For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE HAVING
IMPURITY REGION UNDER ISOLATION REGION (AS AMENDED)

SUPPLEMENTAL PRELIMINARY AMENDMENT

Mail Stop Preliminary Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-referenced application, please amend the application as follows: